Vail Computer Elements Workshop

VCEW Program 2026

All Times Mountain Daylight Time (UTC-6)

Sunday, June 7th

4:00 pm **Check-in Time** for the Lodge at Vail
5:00 pm Gather **in the Lodge lobby** and walk to the **Reception**
6:30 pm **Dinner**:The Lodge at Vail
8:15 pm **Keynote:** *Babak Falsafi EPFL*

Monday, June 8th

7:30 am **Continental Breakfast**
8:30 am **Welcome**

Session 1: Advanced Packaging/Thermals : Eric Fetzer, Thomas Vogelsang

8:45 am *Advanced Packaging Architectures and Technologies for Heterogeneous Integration, Ravi Mahajan, Intel*
9:30 am *Advancing Optical Engines through Heterogeneous Packaging Integration, Krishna Bharath, Lightmatter*
10:15 am **Break**
10:30 am *ΔT: The Architectural Implications of Heat Extraction in XPU Design, David Nellans, Nvidia*
11:15 am *Advanced packaging: what's holding us back?, Subu Iyer, UCLA*
12:00 pm **Lunch**

Session 2: Compute/System Architecture: Yahya Sotoudeh, Rob Chappell

1:00 pm *dMatrix Compute In-Memory, Sudeep Bhoja, dMatrix*
1:45 pm *What can we do about the AI datacenter power explosion?, Sam Naffziger, AMD*
2:30 pm **Break**
2:45 pm *Efficient Compute for the age of AI, Stephen Robinson, Intel*
3:30 pm *Beyond Hacking: Embracing the Promise of Encrypted Computation, Todd Austin, U of Michigan*
5:00 pm **Reception**
6:30 pm **Dinner: Up the Creek**

Session 3: Academic Research: Udit Gupta, Joe Izraelevitz

8:15 pm *Homomorphic encryption and hardware security, Brandon Reagen, NYU*
9:00 pm *Architectures and systems for sparsity in ML, Bahar Asgari, UMD*

Tuesday, June 9th

7:30 am **Continental Breakfast**

Session 4: Consumer Electronics: Atsushi Hasegawa, Ichiro Naka; AI Systems: Tom St. John, Galen Shipman

8:45 am *Hardware Accelerator Design of Advanced Cryptography and Post-Quantum Cryptography, Makoto Ikeda, Univ of Tokyo*
9:30 am *Efficient and Scalable Agentic AI with Heterogeneous Systems, Zain Asgar, Gimlet Labs*
10:15 am **Break**
10:30 am *Asynchronous Tensor Computations on GPUs, Vinod Grover, Nvidia*
11:15 am *When AI Outruns Standards, Keith Underwood, HPE*

12:00 pm **Lunch**
1:00 pm **Free Time to enjoy Vail**
4:00 pm **Planning Session for VCEW 2027**
5:00 pm **Reception**
6:30 pm **Dinner: Lancelot**

Session 5: Applied AI Applications, Cyber Security: Ryan Tabrah, Femi Oluwafemi

8:15 pm *From Algorithms to Infrastructure: What AI Engineering Actually Looks Like, Landen Hughes, Skillsforge AI*
9:00 pm *The New AI Stack: Where Compute, Memory, and Software Are Colliding, Davianne Duarte, AMD*

Wednesday, June 10th

7:30 am **Continental Breakfast**

Session 6: Power, Performance and Sustainability: Rajshree Chabulswar

8:45 am *The Next Era of Telemetry and Performance Monitoring Driven Optimizations, Mike Chynoweth, Intel*
9:30 am *RISC-V Performance Analysis: From the Dark Ages to Modern Day, Beeman Strong, Meta*
10:15 am **Break**
10:30 am *Architecting the Agentic Edge: A Systems and Co Design Perspective, Manu Sabharwal, Qualcomm*
11:15 am *SPEC CPU: Behind The Scenes, Mahesh Madhav, Ampere*
12:00 pm **Awards and Final Thoughts**:

VCEW 2026 Program Committee

Chairs: Jack Harwood, Tufts University; Don Soltis, Intel; Dave Baker, Akeana

Session Chairs

Advanced Packaging/Thermals: Eric Fetzer, Intel and Thomas Vogelsang, Rambus

Compute/System Architecture: Yahya Sotoudeh, Intel and Rob Chappell, Microsoft

Consumer Electronics: Atsushi Hasegawa and Ichiro Naka, University of Tokyo

AI Systems: Tom St John, Gimlet Labs and Galen Shipman, LANL

Power, Performance and Sustainability: Rajshree Chabukswar, Intel and Bri-Matthias Hodge, University of Colorado at Boulder

Applied AI Applications, Cyber Security: Ryan Tabrah, Skillsforge AI and Femi Oluwafemi, Skillsforge AI

University/Academics: Joe Izraelevitz, University of Colorado at Boulder and Udit Gupta, Cornell University

The Vail Computer Elements Workshop is sponsored by VCEW in cooperation with Usenix