

IEEE Computer Society  
2007 VAIL Computer Elements Workshop  
June 24-27, 2007

**2010 & Beyond**

Program Chair: Jay Fleischman, AMD [jay.fleischman@amd.com](mailto:jay.fleischman@amd.com)  
Program Co-Chair: Dale Morris, HP [Dale.morris@hp.com](mailto:Dale.morris@hp.com)

**Sunday June 24** 5 PM Registration, Dinner and Keynote Presentation, **8:00 PM – 9:15 PM**

**RAMP: Research Accelerator for Multiple Processors**  
**Community Vision for a Shared Experimental Parallel HW/SW Platform**  
**John Wawrzynek [johnw@eecs.berkeley.edu]**

Keynote Chair Allan Knies, Intel [Allan.knies@intel.com](mailto:Allan.knies@intel.com)

**Monday June 25**

**Session 1: CPUs**

**8:30 AM – 12:00 noon**

- 1.1 Computer Processing – Next Step
- 1.2 Architecture: SW Transactional Memory
- 1.3 CPU ROCK
- 1.4 Power 6

Chairs Steve Miller, NetApp & Bill Huffman, Tensilica  
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Bill Huott, IBM, [huott@us.ibm.com](mailto:huott@us.ibm.com)

**Session 2: Memory**

**1:00 PM – 4:30 PM**

- 2.1 High Performance SOI eDRAM
- 2.2 DRAM – Past, Present & Future
- 2.3 Emerging Non-Volatile Memories
- 2.4 3D – IC Memory

Chairs Bob Patti, Tezzaron & Jeff Burns, IBM  
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Bob Patti, Tezzaron, [rpatti@tezzaron.com](mailto:rpatti@tezzaron.com)

**Session 3: Enterprise System Trends**

**8:00 PM – 10:15 PM**

- 3.1 3D Internet – Enterprise Enterverse
- 3.2 Blades: Edge to Enterprise Migration
- 3.3 Next Generation Computing Systems

Chairs Lynn Wheeler, Wheeler & Wheeler & Robert Guernsey, IBM  
[lynn@garlic.com](mailto:lynn@garlic.com) [Guernsey@us.ibm.com](mailto:Guernsey@us.ibm.com)  
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David LaPotin, IBM, [dpl@us.ibm.com](mailto:dpl@us.ibm.com)

**Tuesday June 26**

**Session 4: Consumer Electronics & Robots**

**8:30 AM – 12:00 noon**

- 4.1 Wireless 10Gbs – Uncompressed HDTV
- 4.2 Robot Electronics & Info Processing
- 4.3 Pwr Control for Self-Synchronous System
- 4.2 DSC Industry Evolution

Chairs Atsushi Hasegawa, Renesas & Hirofumi Sumi, Sony  
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Yaichi Aoshima, Hitotsubashi Univ., [aoshima@iir.hit-u.ac.jp](mailto:aoshima@iir.hit-u.ac.jp)

**Session 5: Highly Threaded OS/HyperVis.**

**8:00 PM – 10:15 PM**

- 5.1 Support for High Thread Counts in Solaris
- 5.2 Hardware Virtualization Trends
- 5.3 Scalability in System Software

Chairs Jim Hughes, Sun & Ron Perez, IBM  
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Dilma Da Silva, IBM, [dilmasilva@us.ibm.com](mailto:dilmasilva@us.ibm.com)

**Wednesday June 27**

**Session 6: Wireless & Ad Hoc Networks**

**8:30 AM – 12:00 noon**

- 6.1 Cognitive Radio Trends
- 6.1 Policy-based Radios
- 6.2 Cellular Ad hoc Networks
- 6.3 Multi-Standard Radio: Are We There Yet?

Chairs Ed Callaway, Motorola & Tom Conte, NC State Univ.  
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Registration Chair **Ron Bell**  
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The 3D Internet: An Enterverse For The Enterprise"