

# Workshop

## IEEE 2004 Vail Computer Elements

Vail, Colorado  
June 27-30, 2004

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**IEEE Computer Society**

The following is the outline of sessions for the Vail 2004 workshop.  
If you are interested in making a presentation, please contact the session chair.

For further information please contact:

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## Future Visions

Program Chair: Tom Conte, NC State University, [conte@ncsu.edu](mailto:conte@ncsu.edu)  
Program Co-Chair: James Hughes, Storage Tek, [james\\_hughes@storagetek.com](mailto:james_hughes@storagetek.com)

*[Sunday, June 27](#) - Social hour followed by dinner & Keynote Presentation.*

### Visual Perception and Reality

Speaker: Dr. William Glenn, Florida Atlantic University, [glenn@fau.edu](mailto:glenn@fau.edu)  
Keynote Chair: Ron Bell, Math Star, [ron.bell@mathstar.com](mailto:ron.bell@mathstar.com)

*[Monday, June 28](#)*

- Session 1
- PROCESSORS  
Chair: Jim Slager, SuperH, [jslager@iname.com](mailto:jslager@iname.com)  
Chair: Carl Anderson, IBM, [cja@us.ibm.com](mailto:cja@us.ibm.com)
- 1.1 Power 5  
[Mark Sweet](#), IBM
- 1.2 UltraSparc IV  
[Quinn Jacobson](#), Sun Microsystems
- 1.3 Fast14 Technology  
[Mike Becker](#), Intrinsicity
- 1.4 Pentium 4 Prescott  
[Ronak Singhal](#), Intel
- Session 2
- Panel: FPGAs vs. ASICS Panel

Chair: Ron Bell, MathStar, [ron.bell@mathstar.com](mailto:ron.bell@mathstar.com)  
Chair: Bill Mangione-Smith, UCLA, [billms@ee.ucla.edu](mailto:billms@ee.ucla.edu)

2.1 Re-configurable Directions

[Cary Snyder](#), SemiView

2.2 Embedded FPGA Apps

[Parimal Patel](#), Xilinx

2.3 FPGA/ASIC Design Flow

[David Taubenheim](#), Motorola

2.4 Adaptive Computing Processor

[Paul Master](#), QuickSilver

2.5 Re-configurable Design

[Ron Bell](#), MathStar

Session 3

ADV. SEMICONDUCTOR TOOLS

Chair: Robert Guernsey, IBM, [guernsey@us.ibm.com](mailto:guernsey@us.ibm.com)

Chair: Subramanian Iyer, IBM, [ssiyer@us.ibm.com](mailto:ssiyer@us.ibm.com)

3.1 EDA Challenges

[Ted Vucurevich](#), Cadence

3.2 Analysis & Process Variability

[Leon Stok](#), IBM

3.3 Design for Yield Management

[Andrzej Strojwas](#)

*[Tuesday, June 29](#)*

Session 4

CONSUMER ELECTRONICS

Chair: Michinori Nishihara, IBM, [michi@jp.ibm.com](mailto:michi@jp.ibm.com)

Chair: Yoshiaki Hagiwara, Sony, [yoshiaki.hagiwara@jp.sony.com](mailto:yoshiaki.hagiwara@jp.sony.com)

4.1 DVD Player Technology

[Hiroki Mouri](#), Panasonic

4.2 Mobile Multimedia Processor

[Kunio Uchiyama](#), Hitachi

4.3 MPU for Consumer Apps

[Naoki Mitsuishi](#), Renesas

4.4 Processor Design Trade-offs

[Peter Hofstee](#), IBM

Session 5

Panel: BLADES vs. SMP Panel

Chair: Alan Charlesworth, Sun, [alan.charlesworth@sun.com](mailto:alan.charlesworth@sun.com)

Chair: Richard Oehler, NEWISYS, [rich.oehler@newisys.com](mailto:rich.oehler@newisys.com)

5.1 [Alan Charlesworth](#), Sun Microsystems

5.2 [Linton Ward](#), IBM

5.3 [Tom Barclay](#), Microsoft

5.4 [Bruce Lindsay](#), IBM

5.5 [Rich Adkisson](#), HP

*Wednesday, June 30*

Session 6

WIRELESS

Chair: Ed Callaway, Motorola, [ed.callaway@motorola.com](mailto:ed.callaway@motorola.com)

Chair: Kevin Doren, [kevin@doren.org](mailto:kevin@doren.org)

6.1 Software Defined Radio

[John Grosspietsch](#), Motorola

6.2 UWB Communications

[Robert Aiello](#), Staccato Communications

6.3 UWB Propagation

[Kai Siwiak](#), Time Derivative

6.4 The ZigBee Spec

[Robert Heile](#), The ZigBee Alliance

## **Contacts**

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Jim Hughes, StorageTek

Frank Mikalauskas, Hewlett-Packard

John Polhemus, Consultant

Ron Bell, Math Star

Michinori Nishihara, IBM

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If you have any comments or suggestions regarding the Computer Elements web page, please send an e-mail to [gmerckel@unf.edu](mailto:gmerckel@unf.edu).