Workshop
The 2002 Vail Computer Elements
Vail, Colorado
June 23-26, 2002
Sponsored by
IEEE Computer Society

The following is the outline of sessions for the Vail 2002 workshop.

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Invitation & Registration
Design Challenges

Program Chair: R. Brad Older, Intel
Program Co-Chair: James Hughes, Storage Tek

Sunday, June 23

5:00pm - Social Hour followed by dinner & Keynote Presentation.

"The Road to 64 Bits: Intel/HP vs AMD"
Bill Worley, HP & Allan Knies, Intel - Fred Weber, AMD & Wayne Meretsky, AMD.
Moderator Jim Slager, JAZiO

Monday, June 24

Session 1 New Microprocessors
1.1 Intel/HP McKinley
1.2 AMD Sledge Hammer
1.3 NVIDIA GeForce 4
1.4 Complexity vs Power vs Performance

Session 2 SOC & SOI
2.1 Vertical ICs via Through Silicon
2.2 Advances in SOI
2.3 SOC Development Methodologies
2.4 SOC Development for AU1500

Session 3 Internet & Wireless
3.1 Lower Power Spread Spectrum
<table>
<thead>
<tr>
<th></th>
<th>Ad Hoc Networks</th>
<th>Ed Callaway, Motorola</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.3</td>
<td>Internet Road Weather System</td>
<td>David Lambert, UNF</td>
</tr>
<tr>
<td>3.4</td>
<td>Multimedia on the Internet</td>
<td>Horst Clausen, Univ. of Salzburg</td>
</tr>
</tbody>
</table>

**Tuesday, June 25**

<table>
<thead>
<tr>
<th>Session 4</th>
<th>Consumer Electronics</th>
<th>Chairs Michinori Nishihara &amp; Yokshiiaki Hagiwara</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.1</td>
<td>Palm Directions</td>
<td>Bill Stanely, Palm</td>
</tr>
<tr>
<td>4.2</td>
<td>Software System on a Chip</td>
<td>Dave Fotland, Ubicom</td>
</tr>
<tr>
<td>4.3</td>
<td>Consumer Electronics Trends</td>
<td>Yoshiiaki Hagiwara, Sony</td>
</tr>
<tr>
<td>4.4</td>
<td>Digital Video Trends</td>
<td>George Cooper, Adtec</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Session 5</th>
<th>Embedded &amp; Mixed Signal Systems</th>
<th>Chairs Allan Knies &amp; John Alexander</th>
</tr>
</thead>
<tbody>
<tr>
<td>5.1</td>
<td>RF Circuits in a CMOS Design</td>
<td>Stephen Pawlowski, Intel</td>
</tr>
<tr>
<td>5.2</td>
<td>MAP BSP-15</td>
<td>Chris Basoglu, Equator</td>
</tr>
<tr>
<td>5.3</td>
<td>Embedded Intelligent Sensors</td>
<td>John Alexander, UNF</td>
</tr>
</tbody>
</table>

**Wednesday, June 26**

<table>
<thead>
<tr>
<th>Session 6</th>
<th>Systems Interconnect &amp; Packaging</th>
<th>Chair Alan Charlesworth</th>
</tr>
</thead>
<tbody>
<tr>
<td>6.1</td>
<td>Server Revenue Trends</td>
<td>Alan Charlesworth, Sun</td>
</tr>
<tr>
<td>6.2</td>
<td>SMP Technology</td>
<td>Alan Charlesworth, Sun</td>
</tr>
<tr>
<td>6.3</td>
<td>I/O Wars: 3GIO/Infiniband/IP Off-load</td>
<td>Greg Pfister, IBM</td>
</tr>
<tr>
<td>6.4</td>
<td>Gigabit Ethernet Mesh</td>
<td>Ravi Kavuri, StorageTek</td>
</tr>
</tbody>
</table>

**Contacts**

Contacts for the 2002 Vail Computer Elements Workshop chairs are available on the [Vail 2002 contacts page](#).

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Jerry Merckel, University of North Florida  
Joel Boney, HP  
Frank Mikalauskas, Compaq  
John Polhemus, Consultant  
Ron Bell, MicroLinear  
Michinori Nishihara,IBM

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