Theme: Possible Futures

Sunday June 22nd  5 PM Registration and Dinner.  Keynote Presentation (8:00 PM – 9:15 PM)
Key Note Speaker: Robin Hanson (George Mason University)  rhanson@gmu.edu

Keynote: Prediction Markets
Giving the Truth Orientation of Engineers to Everyone Else

Program Chair:  Mark S. Miller (Google)  erreights@google.com
Program Co-Chair:  Brian Hirano (Oracle)  brian.hirano@oracle.com
Key Note Chair: Jim Hughes (Seagate)  jphughes@mac.com

Monday June 23rd
Session 1: Security
9:00 AM – 12:00 noon
Securing Software Defined Networks
Shill (Caps-based Scripting)
Secure Coding
Chairs: Joe Politz (Brown Univ), Dean Sutherland (CERT)

Arjun Guha (UMass Amherst)  arjun@cs.umass.edu
Scott Moore (Harvard)  sdmooar@fas.harvard.edu
Daniel Plakosh (CERT)  dpplakosh@cert.org

does@sunrisemicro.com

Session 2: Concurrency in Prog. Languages
1:00 PM – 3:30 PM
EM2 Scalable Multicore Architecture
Harnessing Hardware Parallelism with Erlang
Resilient & Petascale X10
Chairs: Brian Hirano (Oracle), Jan Medved (Cisco)

Mieszko Lis (MIT)  mieszko@csail.mit.edu
Jim Larson (Google)  jimlarson@google.com
David Cunningham (Google)  sparkprime@gmail.com

Session 3: Imaging and Signal Processing
3:40 PM – 4:30 PM
Full-spec UHDTV-2 Video System
Imaging Technologies
Highlights of the Imaging World
3D CMOS Image Sensors and System
Panel Discussion
Chairs: Hirofummi Sumi (TSMC), Makoto Ikeda (Univ. of Tokyo)

Hiroshi Shimamoto (NHK)  shiomamot.h-iu@nhk.or.jp
Eric Fossum (Dartmouth College)  Eric_R_Fossum@dartmouth.edu
Boyd Fowler (Google)  boydfowler@google.com
Masayuki Shimura (Sony)  Masayuki.Shimura@jp.sony.com
Hirofummi Sumi (TSMC), Makoto Ikeda (U. Tokyo)
All

Tuesday June 24th
Session 4: Wireless and Networking
9:00 AM – 12:00 noon
Wireless IP and Embedded MCUs
Network-Centric Programming Languages
Scaling High Performance Managed Clusters
Chairs: Ed Callaway (Sunrise Micro Devices), Don Banks (Cisco)

David Flynn (ARM)  David.Flynn@arm.com
Jon Rossie (Cisco)  jorossie@cisco.com
Landon Noll (Cisco)  chongo@cisco.com

Session 5: Consumer Electronics
8:00 PM – 10:30 PM
Surprise LCD Uses: 3D Vision & G-Sync
A Novel Power Play for Distributed LEDs
Energy Efficient Many-Core SoC
Chairs: Atsushi Hasegawa (Renesas), Yoshio Masubuchi (Toshiba)

Hirokumi Shimamoto (NHK)  shiomamot.h-iu@nhk.or.jp
Tsumu Shimomura (Neofocal Systems)  tsutomu@neofocal.com
Hiroyuki Usui (Toshiba)  hiroyuki1.usui@toshiba.co.jp

Wednesday June 25th
Session 6: Processors
9:00 AM – 12:00 noon
Heterogeneous Computing and its Low Power Considerations
Xbox One: Next Gen Game Processor
Freescale’s Advanced IO Processor
Chairs: Yahya Sotoudeh (Intel), Pete Wilson (Freescale)

Roy Ju (Mediatek)  royju@mediatek.com
Sebastien Nussbaum (AMD)  sebastien.nussbaum@amd.com
John Sell (Microsoft)  jsell@microsoft.com

Freescale’s Advanced IO Processor
Brian Kahne (Freescale)  bkahne@freescale.com

Planning Session: IEEE Vail 2015, 4PM – 5:30PM  All invited to participate.

Local Arrangements, VCEW Support
john.m.polhemus@lmco.com

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Committee Vice Chair Ray Barrett  raybarrett@comcast.net

Asia Pacific Chair Atsushi Hasegawa  atsushi.hasegawa.qx@renesas.com
Committee Chair Emeritus Gerald Merckel  gmerckel@unf.edu