

Vail Computer Elements Workshop 2018

Sunday, June 24, 2018

5:00-6:30 pm Social Hour: **AD-4 Condo** (Directions available at the Christiania Hotel)

Dinner Restaurant: Left Bank (substitute for the closed Campo Di Fiore) 6:30- 8:00 pm

Keynote: Speed doesn't matter if you can't drive it, Stephanie Rosenbaum, TecEd, Inc. 8:00-9:30 pm

Monday, June 25, 2018

Breakfast: Sarah's Lounge 7:30-9:00 am

Opening Words - Jim Hughes 9.00am

Session I Consumer Electronics

Chairs: Atsushi Hasegawa, Renesas; Yoshio Masubuchi, Toshiba Memory

9:00-9:45 am Endpoint Al Chip, Atsushi Hasegawa, Renesas 9:45- 10:30 am 2 Near-Data Computing, Jun Deguchi, Toshiba Memory

Break & Group Picture 10:30-10:45 am

10:45-11:30 am MPEG Media Transport Technologies and its application to immersive media communication

services,

Takayuki Nakachi, NTT Side Channel Attacks in the Real World Bot, Ordo Labs 11:30 -12:15 pm

12:15-1:30 pm Lunch: Sarah's Lounge

Session 2 Processors

Chairs: Yahya Sotoudeh Intel; Steve Miller Intel

1:30-2:15 pm Cavium Thunder X2 Processor Rabin Sugumar, Cavium

RISC-V FireSim/FireBox Sagar Karandikar, UC Berkeley ADEPT 2:15-3:00 pm

Lab

3:00 -3:15 pm Break

3. ARM Machine Learning Processor Ian Bratt, ARM 3:15-4:00 pm 4. AMD EPYC Jay Fleischman, amD 4:00-4:45 pm

> **Break** 4:45-5:00 pm

AD-4 Condo 5:00-6:15 pm **Social Hour:**

6:15-8:00 pm **Dinner Restaurant: The Lancelot**

Session 3

8:00-9:30 pm From Exabytes to Nobel Prizes Fons Rademakers, CERN

Tuesday, June 26, 2018

Breakfast: Sarah's Lounge 7:00-8:30 am

8:30 am Session 4 Database + Processors

Chairs: Pankaj Mehra, Samsung, Arun Majumdar, Kyndi, Inc

8:30-9:15 am

Systems and Machine Learning

Amir Khorsrowshahi, Intel (Nervana)

9:15-10:00 am 2 BigStream Maysam Lavasani, BigStream

10:00 am 10:15 am Break

10:15-11:00 am 3 Massive Shared Memory on Commodity Processors Steffan Persvold, Numascale

11:00-11:45 am 4 3DIC Don Draper, ProPrincipia

11:45-1:00 pm Lunch Sarah's Lounge

1:00-4:00 pm Afternoon Free

4:00-5:00 pm Planning Meeting in Sara's Lounge

Optional, but please attend to help plan VCEW 2019

Refreshments provided

5:00-6:00 pm Social Hour: AD-4 Condo

Session 5 Accelerators

Chairs: Mike Nelson, Intel; Steve Fields, IBM

8:00-8:45 pm I Microsoft Brainwave: Flexible Deep-Learning Accelerator Eric Chung, Microsoft Research
8:45-9:30 pm 2 Intel's Acceleration Stack Tool Chain for Xeon CPUs with FPGAs Michal Skiba, Intel (PSG)

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Wednesday, June 27, 2018

7:00-8:30 am Breakfast: Sarah's Lounge

Session 6 Programming Languages + Internet Economy

Chairs: Andy Rudoff, Intel; Pete Wilson, Kiva Design Groupe; Rich Zippel, Google; John Kelsey, NIST

8:30-9:15 am I Computer Architecture Considered Harmful Pete Wilson Kiva Design Groupe
9:15-10:00 am 2 Marketing for the 21st Century: Online Behavioral and Contextual Ads George Salem

10:00-10:15 am Break

10:15-11:00 am 3 Enhancing Java for Persistent Memory Steve Dohrmann Intel
11:00-11:45 am 4 The NIST Beacon and Public Randomness John Kelsey NIST

11.45 Closing words - Jim Hughes.

11:45-1:00 pm Lunch