

IEEE Computer Society
2011 VAIL Computer Elements Workshop
June 26-29, 2011

Expanding Frontiers

Program Chair: Yahya Sotoudeh, Intel, yahya.sotoudeh@intel.com
Program Co-Chair: Sam Naffziger, AMD, Samuel.naffziger@amd.com

Sunday June 26 5 PM Registration, Dinner and Keynote Presentation (**8:00 PM – 9:15 PM**)

Throughways, By-ways and Cul-de-sacs

Key Note Chair: Jim Hughes, jhughes@huawei.com

Key Note Speaker: Dr. Anita Jones, jones@cs.virginia.edu

Monday June 27

Session 1: Processors

8:30 AM – 12:00 noon

- 1.1 Bobcat – Low Power Processor
- 1.2 Server Storage Convergence
- 1.3 Low Power TI MSP 430 with FRAM
- 1.4 IBM z196 Microarchitecture

Chairs Sam Naffziger, AMD, & Steve Miller, NetApp

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Mike McGrath, Intel, Michael.mcgrath@intel.com
Christy She, TI, c-she@TI.com
Eric Schwarz, IBM, eschwarz@us.ibm.com

Session 2: High Performance Wireless

1:00 PM – 4:30 PM

- 2.1 Multi-Protocol SW Radio
- 2.2 MIMO for Reliable Links
- 2.3 100GMAC 4G Core
- 2.4 Artic Sand

Chairs Bill Huffman, Tensilica, & Ray Barrett, American R & D

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Jerzy Zurek, GDYNIA Maritime Academy jez@kpl.pl
Chris Rowen, Tensilica, rowen@tensilica.com
Nadia Shalaby, MIT, nadia@sloan.mit.edu

Session 3: Security & More

8:00 PM – 10:15 PM

- 3.1 Disaster Information Computing
- 3.2 Security Vision for Intel
- 3.3 Secure Distributed Computing
- 3.4 Computer Animated Films

Chairs Jim Simak, Sys Innovators, & Dan Hettena, Green Hills SW

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Mark Miller, Google, erights@google.com
Gene Lee, Disney, gslee007@gmail.com

Tuesday June 28

Session 4: Consumer Electronics

8:30 AM – 12:00 noon

- 4.1 HS Vision Processing
- 4.2 HS Switching Circuits
- 4.3 Ultra High Optical Network Capacity
- 4.4 4K Super-HD Video Streaming

Chairs Atsushi Hasegawa, Renesas, & Hirofumi Sumi, Sony

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Tatsuya Fujii, NTT Network Innovation Labs, fujii.tatsuya@lab.ntt.co.jp

Planning Session: IEEE Vail 2012, 4PM – 5:30 PM All invited to participate.

Session 5: Potpourri

8:00 PM – 10:15 PM

- 5.1 Adaptive Power Management
- 5.2 Next Gen Blue Gene
- 5.3 Memristors
- 5.4 Power Management via Social Media

Chairs Brian Hirano, Oracle, & Jay Fleischman, AMD

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Mike Floyd, IBM, mfloyd@us.ibm.com
Brad McCredie, IBM, mccredie@us.ibm.com
Steve Wald, Boise State U., swald@ieee.org
Steve Hammond, Lowfoot, shammond@lowfoot.com

Wednesday June 29

Session 6: Embedded Systems

8:00 AM – 12:00 noon

- 6.1 DSP Architecture
- 6.2 HEXAGON - DSP for Mobile Apps
- 6.3 Ethernet Clock Synchronization
- 6.4 Embedded SW –Concept to Reality

Chairs David Baker, Green Hills SW, & Sebastien Nussbaum, AMD

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