

IEEE Computer Society  
2008 VAIL Computer Elements Workshop  
June 22-25, 2008

**What's New?**

Program Chair: Steve Miller, NetApp, [scm@netapp.com](mailto:scm@netapp.com)  
Program Co-Chair: Jay Fleischman, AMD, [jay.fleischman@amd.com](mailto:jay.fleischman@amd.com)

**Sunday June 22** 5 PM Registration, Dinner and Keynote Presentation, **8:00 PM – 9:15 PM**

**"The Future is Parallel: What's a programmer to do?"**

Guy Steele, Sun Microsystems, [guy.steele@sun.com](mailto:guy.steele@sun.com)

Keynote Chair Jim Hughes, Sun, [james.hughes@sun.com](mailto:james.hughes@sun.com)

**Monday June 23**

**Session 1: Intelligent Networks**

**8:30 AM – 12:00 noon**

- 1.1 OptiPlanet–Global Computing Via Light Paths
- 1.2 Global Envir for Network Investigations
- 1.3 Network Evolution: Tech & Applications
- 1.4 Military Communications & Networking

Chairs David LaPotin, IBM & Rick McGeer, HP Labs

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Jason Leigh, Univ of Illinois, [spiff@uic.edu](mailto:spiff@uic.edu)

Chip Elliott, BBN Technologies, [celliott@bbn.com](mailto:celliott@bbn.com)

Charles Kalmanek, AT&T Labs, [crk@research.att.com](mailto:crk@research.att.com)

Kenneth Young, Telcordia, [kcy@research.telcordia.com](mailto:kcy@research.telcordia.com)

**Session 2: Next Gen Database Systems**

**1:00 PM – 4:30 PM**

- 2.1 Blue Gene
- 2.2 Super Computing & Mainstream Components
- 2.3 Roadrunner-Heterogeneous Supercomputing
- 2.4 Programming for Super Parallel Computing

Chairs Damir Jamsek, IBM & Bob Lucas, USC

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Todd Takken, IBM, [takken@us.ibm.com](mailto:takken@us.ibm.com)

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**Session 3: Cognitive Computing & User I/Fs**

**8:00 PM – 10:15 PM**

- 3.1 Data Center Management User Interface
- 3.2 Computers for Cognition
- 3.3 I/F Design – The Good, Bad & Ugly

Chairs Dale Morris, HP & Niraj Srivastava, HP

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Mark Anspach, HP, [mark.anspach@hp.com](mailto:mark.anspach@hp.com)

**Tuesday June 24**

**Session 4: Displays & Packaging**

**8:30 AM – 12:00 noon**

- 4.1 System LSI for Digital Broadcasting Apps
- 4.2 Electronic Paper Network System
- 4.3 Organic Electronics on Plastic Films
- 4.4 Multi-layer Stacked Devices Fabrication

Chairs Atsushi Hasegawa, Renesas & Hirofumi Sumi, Sony

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Nobuaki Miyakawa, Honda Research Inst., [miyakawa@jp.honda-ri.com](mailto:miyakawa@jp.honda-ri.com)

**Session 5: Embedded Systems & Apps**

**8:00 PM – 10:15 PM**

- 5.1 Multi-threaded Multi-core Embedded Systems
- 5.2 MIPS 74K 1GHz+
- 5.3 Skyplex Satellite Terminal Design
- 5.4 European Research-Embedded Systems

Chairs Mario Vinsani, ERIX & Craig Shinnars, MIPS

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**Wednesday June 25**

**Session 6: Future Technologies**

**8:30 AM – 12:00 noon**

- 6.1 Nanoelectronics Research Challenges
- 6.2 Carbon-based Electronics
- 6.3 Quantum Computing
- 6.4 Spintronics

Chairs Ray Barrett, Allegro & Bill Gallagher, IBM

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