

Workshop

The 2003 Vail Computer Elements

Vail, Colorado
June 22-25, 2003

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The following is the outline of sessions for the Vail 2003 workshop.
If you are interested in making a presentation, please contact the session chair.

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Expanding Horizons

Program Chair: James Hughes, Storage Tek, james_hughes@storagetek.com
Program Co-Chair: R. Brad Older, Intel, brad.older@intel.com

[Sunday, June 22](#) - Social hour followed by dinner & Keynote Presentation.

"Privacy & Security," Valerie McNevim, State of Colorado
Keynote Chair: Ron Bell, Micro Linear, bell.ron@microlinear.com

[Monday, June 23](#)

Session 1

PROCESSORS

Chair: Jim Slager, Jazio, jslager@jazio.com

Chair: Carl Anderson, IBM, cja@us.ibm.com

- 1.1 Tera-op Adaptive Processing, [Chuck Moore](#), U. of Texas
- 1.2 Future Server Processors, [Brad McCredie](#), IBM
- 1.3 Dual Core Itanium Power Management, [Sam Naffziger](#), HP
- 1.4 Future Microprocessors, [Jim Kahle](#), IBM

Session 2

SECURITY

Chair: Bill Worley, NextGenInet, worley@nextgeninet.com

Chair: Jim Hughes, Storage Tek, james_hughes@storagetek.com

- 2.1 OS Security, [Brian LaMacchia](#), Microsoft
- 2.2 Cybercrime, [Dave Mahon](#), FBI

- 2.3 Storage Encryption, [Jim Hughes](#), Storage Tek
- 2.4 Signature Only Smart Cart, [Lynn Wheeler](#), FirstData

Session 3

SYSTEM POWER ISSUES

Chair: Ray Barrett, Maxim, rlb@design.mxim.com

Chair: Bob Montoye, IBM, montoye@us.ibm.com

- 3.1 CPU Power Design, [Ray Barrett](#), Maxim
- 3.2 Powering Handheld Displays, [Alex Gusinov](#), Sipex
- 3.3 System On Chip, [Subramanian Iyer](#), IBM
- 3.4 Portable Systems, [Bob Montoye](#), IBM

Tuesday, June 24

Session 4

CONSUMER ELECTRONICS

Chair: Michinori Nishihara, IBM, michi@jp.ibm.com

Chair: Yoshiaki Hagiwara, Sony, yoshiaki.hagiwara@jp.sony.com

- 4.1 AI in Strategy Games, [David Fotland](#), Smart Games
- 4.2 Portable Phone Directions, [Makoto Miyake](#), Mitsubishi
- 4.3 Networked Entertainment, [Naohisa Ohta](#), Sony
- 4.4 Low Power Electronics, [Rahim Khoie](#), U. of the Pacific

Session 5

COMMUNICATIONS

Chair: Ed Callaway, Motorola, Ed.callaway@motorola.com

Chair: Ravi Kavuri, StorageTek, Ravi_kavuri@storagetek.com

- 5.1 IEEE 802.15.4 RF Transceiver, [Fred Martin](#), Motorola
- 5.2 MASI Comm Processor, [David Fotland](#), Ubicom
- 5.3 Ultra-Wideband Comm Systems, [Roberto Aiello](#), Discrete Time
- 5.4 Gb Ethernet, [Atiq Ahamad](#), StorageTek

Wednesday, June 25

Session 6

SYSTEMS

Chair: Alan Charlesworth, Sun, Alan.charlesworth@sun.com

Chair: Dave Baker, A6Labs, Db@a6labs.com

- 6.1 Large Server Trends, [Alan Charlesworth](#), Sun
- 6.2 Embedded Systems, [Tom Conte](#), NC State Univ.
- 6.3 SOC IP SAN, [Gary McMillian](#), aRTiCA Semiconductor
- 6.4 Tera-Tory Processor, [David Baker](#), A6Labs

Contacts

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Jerry Merckel, University of North Florida

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Frank Mikalauskas, Hewlett-Packard
John Polhemus, Consultant
Ron Bell, Micro Linear
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If you have any comments or suggestions regarding the Computer Elements web page, please send an e-mail to gmerckel@unf.edu.