The 2000 Vail Computer Elements

Vail, Colorado June 25-28, 2000

Sponsored by

IEEE Computer Society

The following is the outline of sessions for the Vail workshop as of May 5, 2000. Invitation letter follows session information. Click here for registration form.

For further information please contact:

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Sunday Afternoon / Evening, June 25

4:00pm - 5:00pm - Technical Committee Meeting in Sarah's Lounge (all welcome)

5:00pm - Social Hour Followed by Dinner

8:00 pm - Evening Keynote Address: Bob Guernsey, IBM

Monday, June 26

8:15 am - 8:30 am - Announcements and Introductions

8:30 am - 12:00 noon

Session 1 - High End Microprocessors

Chair: Jim Slager

1a. Title: *Intel Itanium*. Speaker: Hans Mulder, Intel
1b. Title: *IBM Power 4*. Speaker: Jim Kahle, IBM
1c. Title: *AMD Athlon*. Speaker: Jay Picket, AMD
1d. Title: *Intel Willamette*. Speaker: Dave Sager, Intel

1:00 pm - 4:30 pm

Session 2: On-chip Noise and Limits of CMOS

Session Chair: Carl Anderson

2a. Title: *Noise in CMOS VLSI Chips*. Speaker: Ken Shepard, Columbia University 2b. Title: *Noise, Package and Performance Considerations in Elastic I/O Design*.

Speaker: Dan Dreps, IBM

2c. Title: *IBM Future CMOS Technology for ASICS and Microprocessors*. Speaker: Clement Wan, IBM

2d. Title: JAZiO High Speed Interconnect. Speaker: Jim Slager, JAZiO

2e. Title: Future CMOS Technology. Speaker: Tahir Ghani, Intel

8:00 pm - 10:00 pm

Session 3: Servers, High End CMOS Machines and RAS

Session Chairs: Linton Ward, Alan Charlesworth

3a. Title: InfiniBand IO Architecture. Speaker: Renato Recio

3b. Title: S/390 System Architecture Directions. Speaker: Guru Rao, IBM

3c. Title: Compaq Wildfire. Speaker: David Fenwick

Tuesday, June 27

8:15 - 8:30 - Announcements

8:30 am - 12:00 noon

Session 4: Networks, Communications, and Security

Session Chairs: David Baker, Jack Bigham

4a. Title: Utilizing Unlimited Bandwidth for Global Collaboration. Speaker: Mike

Mott, Boeing

4b. Title: Persistent Information Security and its Effects on Networking. Speaker: Jim

Hughes, Storage Tek

4c. Title: Compilation Models for DSPS. Speaker: Tom Conte, N.C. State University

4d. Title: 3rd Generation Cell Phone Base Station Beam Former Applications for BOPS

& DSP. Speaker: Dave Strube

Tuesday afternoon:

Free for individual interaction, recreation and planning.

8:00pm - 10:30pm

Session 5: Digital Information Appliances in Consumer Space

Session Chairs: John O'Donnell, Michinori Nishihara

5a. Title: RF Technology for Consumer Space. Speaker: Ray Barrett, Motorola

5b. Title: TBD. Speaker: Peter Dribble, Microware

5c. Title: Information Appliances in Multimedia Networks, (90 minutes). Speaker:

Yuichiro Takagawa, NTT East

Title: IRDA-ISDN Pay Phone System Short Talk 2: Ryuchi Saithoh, NTT

Wednesday, June 28

8:30am - 12:00 noon

Session 6: Multithreading & Microprocessor Arch. Innovations

Session Chairs: Allan Knies, Bob Montoye

6a. Title: Transmeta Crusoe. Speaker: Godfrey D'Souza, Transmeta

6b. Title: AS/400 and RS/6000 System Architecture. Speaker: Rick Eickemeyer, IBM

6c. Title: Speculative Multi-threading in MAJC. Speaker: Marc Tremblay, Sun

6d. Title: SMT, SMP, and the Compag 21464. Speaker: Mathew Reilly

2000 IEEE Computer Elements Vail Workshop

June 25 -28, 2000

Invitation

The IEEE Technical Committee Elements invites you to participate in the 2000 Computer Elements Workshop to be held in Vail, Colorado from June 25 to June 28, 2000 at the Christiania Lodge on Hansen Ranch Road in Vail. An excellent program has been created by the program committee under the able leadership program chair Joel Boney.

The program starts at 5:00 p.m. on Sunday June 25. We hold the keynote address on Sunday evening, and have six sessions over the next three days. We encourage all attendees to contribute to the success of the workshop through their active participation. This could occur during discussion periods after the talks, at breaks, and at meals. This workshop has been structured to allow many opportunities for informal technical discussions. As in the past, we will continue to have an environment which allows an undocumented exchange of leading edge technical experiences from which al attendees can benefit. In accordance with IEEE workshop rules, no material will be published, nor will cameras or tape recorders be permitted. No proprietary information will be discussed, however, questions of technical nature may be discussed at length.

Attendance will be limited to ensure a workshop atmosphere, so please get your registration in early! The program for the workshop and registration information is on the following pages. The program is quite firm, although there may be some changes in content or order of presentation. Participants are encouraged to attend the entire workshop; no partial registration is offered. More details can be found at our web page located at www.computer.org/tab/tclist/tcce.htm then click on Computer Elements. The total fee of \$635 (\$585 for IEEE members) covers the IEEE workshop registration fee, lodging, and all meals from Sunday dinner through Wednesday. Registration is due by June 5, 2000. After this date rooms may not be available. Please feel free to contact the Registrar, John Polhemus (303-781-6190) for information or questions. I look forward to seeing you at the workshop for an interesting and fruitful exchange of ideas.

Van service from the Denver airport can be arranged by calling Colorado Mountain Express at 800-525-6363.

Please check the box to join the Computer Elements Technical committee. It costs nothing and demonstrates to the Computer Society that we are a technically interesting group to belong to. Thank you,

Jerry Merckel

Chairman, IEEE Technical Committee on Computer Elements

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Contacts

Contacts for the 2000 Vail Computer Elements Workshop chairs are available on the Vail 2000 contacts page.

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If you have any comments or suggestions regarding the Computer Elements web page, please send an e-mail to:

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