

Program - Vail 1999

1999 Vail Computer Elements Workshop

June 27th – 30th 1999, Vail Colorado

Sponsored by the IEEE Computer Society's Technical Committee (TC) on Computer Elements

TC Chair: Douglas Wescott, IBM 914-435-8652

Vice Chair Jeff Yetter, Hewlett Packard 970-898-3249

Japan Chair Michinori Nishihara, IBM Japan 81-0775-87-6110

Europe Chair: Mario Vinsani, +39.02.9381559

Vail Workshop:

General Chair Jeff Yetter, Hewlett Packard 970-898-3249

Program Chair Joel Boney, Hewlett Packard 970-898-6518

Arrangements Ron Bell, Micro Linear 408-369-5477

Registration John Polhemus, Consultant 303-781-6190

Treasure John Polhemus, Consultant 303-781-6190

Sunday Evening, June 27th:

4:00pm: Technical Committee Meeting. All Welcome

5:00pm: Social Hour Followed by Dinner

8:00pm: Evening Keynote Address: Microprocessors: Current and Future Trends

Speaker: Keith Diefendorff, Micro Design Resources

Monday, June 28th:

Session 1: Large Web and Commercial Servers

Session Chairs: John Norris, Bob Guernsey

1a. Title: *HP N Class Commercial Server*

Speaker: Bill Bryg, HP

1b. Title: *Fault Tolerance and Data Integrity in Non Stop Himalaya Computer Systems*

Speaker: Bob Jardine, Compaq

1c. Title: *Ultra-Sparc III Based Mid and High End Servers*

Speaker: Alan Charlesworth, Sun Microsystems

1d. Title: *Large Web Site Architecture*

Speaker: Jay Casler, IBM

Session 2: IA-64 Architecture Details

Session Chair: Dave Fotland, Co-chair: Joel Boney

2a. Title: *Instruction Repertoire, Functional Units, and Dispersal*

Speaker: David Fotland, HP

2b. Title: *Architectural Enhancements for high ILP*

Speaker: Allan Knies, Intel

2c. Title: *IA-64 Register Model: Stack & Rotation*

Speaker: Dale Morris, HP

2d. Title: *Compiling for IA-64*

Speaker: Carol Thompson, HP

Session 3: Low Power and Consumer Electronics

Session Chair: Nishihara-san, Co-chair: Senta-san,

3a. Title: *MTCMOS / SIMOX Technology: Potential and Future Applications*

Speaker: Yuichi Kado, NTT

3b. Title: *Sony Playstation 2 , Motivation, Requirements and Architecture Definition*

Speaker: Hidetaka Magoshi, Sony Computer Entertainment

3c. Title: *Sony Playstation 2, Implementation Technology for Emotion Engine*

Speaker: Mitsuo Saito, Toshiba

Tuesday, June 29th:

Session 4: IA-32

Session Chairs: Jim Slager, Bob Montoye.

4a. Title: *Pentium III with SSE*

Speaker: Shreekant (Ticky) Thakkar, Intel

4b. Title: *AMD K7*

Speaker: Jay Pickett, AMD

4c. Title: *Centaur WinChip*

Speaker: Dave Witt, Centaur

4d. Title: *IA-32 Summary*

Speaker: Keith Diefendorff, Micro Design Resources

Session 5: Microprocessors, Complex Chips, Systems on a Chip and Embedded DRAM

Session Chairs: Nishihara-san, Jay Fleischman,

5a. Title: *Macro Design, Architecture, Test and Application of Embedded DRAM*

Speaker: John Barth Jr., IBM

5b. Title: *Perspective on SSI and Impact on Memory*

Speaker: Richard Mattick, IBM

5c. Title: *Server Oriented Microprocessor Optimizations*

Speaker: Chuck Moore, IBM

5d. Title: *The Changing Landscape of System-on-a-Chip Design*

Speaker: Bill Lee, IBM

Wednesday, June 30th:

Session 6: eCommerce

Session Chairs: Bill Worley, Herb Schorr

6a. Title: *B2B and B2C Value Networks*

Speaker: George Spix, Microsoft

6b. Title: *Next Generation eCommerce Applications*

Speaker: Pedro Szekely, ISI

6c. Title: *eCommerce at HP*

Speaker: Joe Pato, HP

6d. Title: *Advanced Encryption Standards*

Speaker: Bill Worley, HP

Last updated: 6/17/99 10:55 AM