

Program - Vail 1997

1997 IEEE

Computer Elements Vail Workshop

Vail, Colorado

June 22-25, 1997

Workshop Theme: "Technology Moves Too Fast To Worry About Themes!"

Sunday, June 22, 1997

Evening Keynote Address: "The Road to Microprocessor Autonomy"

Mike Johnson, Advanced Micro Devices.

Session Details

Monday, June 23, 1997

Session One: Cryptography and Security in Internet Commerce (8:30 am)

Session Chair: Bill Worley, Hewlett Packard Company.

Co-chair: Herb Schorr, USCISI

1.a. "The State Of The Art Of Encryption",

Speaker: Jeff Schiller, Massachusetts Institute of Technology, Cambridge, MA

1.b. "Encryption At Hewlett Packard"

Speaker: Joe Pato, Hewlett Packard Company, Cupertino, CA.

1.c. "Encryption at IBM",

Speaker: Hamid Ahmadi, IBM Watson Research Lab, Hawthorne, NY.

1.d. "Encryption at Microsoft"

Speaker: George Spix, Microsoft Corp., Redmond, WA.

Session Two: New ISA's (DSP and MMX) (1:30 pm)

Session Chair: Ron Bell, LSIlogic

Co-Chair: Jeff Yetter, Hewlett Packard Company

2.a. "CDSP"

Speaker: Peter Koeppen, Texas Instruments

2.b. "Extending Existing Architectures to Optimize Stream Processing"

Speaker: James Wei, Fusion Micro Media

2.c. "What does MMX(tm) mean?"

Speaker: Larry Mennemeier, Intel Corporation.

2.d. "Do instruction sets matter anymore?"

Speaker: Ron Bell, LSI Logic

Session Three: Open Mic Session (8:00 pm)

Session Chair: Greg Blanck

"Open Mic" presentation & discussion of timely subjects.

Session Four: IC Technology and Embedded Memories (8:30 am)

Session Chair: Carl Anderson, IBM

Session Co-Chair: Hirohsi Yoshimura, NTT

4.a. "The Future of CMOS Technology"

Speaker: Ian Young, Intel Fellow, Intel Corp.

4.b. "High Speed Silicon Germanium Bipolar and BiCMOS Technology"

Speaker: Seshadri Subbanna, IBM East Fishkill

4.c. "The Limits of CMOS"

Speaker: Lewis M. Terman, IBM Watson Research Center

4.d. "DRAM Embedded Logic Overview - Potential and Future Applications"

Speaker: Hikaru Hida, NEC Corp.

4.e. "Impact of CMOS/S IMOX on low-power high speed LSIs"

Speaker: Yusuke Ohtomo, NTT

Session Five: Next Generation Microprocessors (8:00 pm)

Session Chair: Jim Slager, Hitachi

5.a. "IBM S/390"

Speaker: Charles Webb, IBM.

5.b. "HP PA8500"

Speaker: Jay Fleischmann, Hewlett Packard.

5.c. "DEC Alpha 21264",

Speaker: Dan Leibholz, DEC.

5.d. "Intel Mobile Microprocessors"

Speaker: Chip Krauskopf, Intel.

Wednesday, June 25, 1997

Session Six: DVD Applications (8:30 am)

Session Chair: Greg Blanck, Axil Computers

Co-Chair: Yoshiaki Hagiwara, Sony

6.a. "DVD Technology Trends"

Speaker: Mr. Takao Ihashi, Sony Corporation, Tokyo, Japan.

6.b. "Software and Hardware DVD Solutions"

Speaker: George Haber; Zoran Corporation/Compcore, Santa Clara, CA.

6.c. "DVD Drive Technology"

Speaker: Hiroharu Satoh, Toshiba.

Workshop Ends Wednesday June 25, 1:00pm