Program - Vail 1996

1996 IEEE

Computer Elements Vail Workshop

Vail, Colorado
June 23-26, 1996
Workshop Theme: Back to the Future
Sponsored by the IEEE Computer Society's
Technical Committee (TC) on Computer Elements

Sunday, June 23, 1996

Evening Keynote Address: Microprocessor Outlook: 1997-2000"
Michael Slater, MicroDesign Resources.
Session Details

Monday, June 24, 1996

Session One: Mass Storage Technology and I/O Subsystems(8:30am)
Session Chair: Bob Montoye, IBM Almaden Research Center, San Jose, CA
Co-chair: Jeff Connell, Amdahl Corp., Sunnyvale, CA
1 .a. "Disk Drive Technology: The Next 50 Years",
Speaker: Currie Munce; IBM Almaden Research Center, San Jose, CA.
1 .b. "Holographic Storage",
Speaker: Geoffrey Burr; IBM Almaden Research Center, San Jose, CA.
1 .c. "Gigabit Ethernet",
Speaker: Howard Johnson; Olympic Technology Group
1 .d. "Fibre Channel Mass Storage",
Speaker: Richard Taborek; Amdahl Corp., Sunnyvale, CA.
Session Two: Systems on a Chip: Large ASIC Design (1:30pm)
Session Chair: Laszlo Gal, Motorola, Austin, TX. (Calvin Harrison acting)
2.a. "Design Challenges for One-Million-Gate ASICs"
Speaker: Mike Trick; IBM Microelectronics, Essex Junction, VT.
2.b. "System on a Million Gate Chip: Design Tools for Fast Turn-Around"
Speaker: Merrill Hunt; Cadence Design Systems, San Diego, CA.
2.c. "Million Transistor Mixed-Signal Designs"
Speaker: Dave Baker; Brooktree Corp., San Diego, CA.
2.d. "Hardware/Software Co-Design of Systems on a Chip"
Speaker: Calvin Harrison; Motorola, Austin, TX.
Session Three: Display Technology I Panel Session (8:00pm)
3.a "LCD Displays"
Speaker: Zenzo Tajima, Hitachi Ltd., Tokyo, Japan.
Panel Session: Open Microphone

Tuesday, June 25, 1996

Session Four: SMP and Cluster Systems & Interconnects (8:30am)
Session Chair: Cheng Kong; Tandem Computers, Cupertino, CA.
Co-Chair: Joel Boney, HaL Computer Systems, Campbell, CA.
4.a. "Issues and Non-Issues in the Design of Clusters"
Speaker: Greg Pfister; IBM Corp., Austin, TX.
4.b. "How to Break Bus/Backplane Bottlenecks for SMP Without
Breaking the Bank"
Speaker: Wolf Weber; HaL Computer Systems, Campbell, CA.
4.c. "Remote Distributed Memory on Clustered NT Workstations:
NT Clusters on ServerNet",
Speaker: Gary Campbell; Tandem Computers, Cupertino, CA.
4.d. "Building a General Purpose Multi-computer: The Solaris MC Approach",
Speaker: Madhusudhan Talluri; Sun Labs, Mountain View, CA.

Afternoon free for technical interchange

Session Five: High Perf. Circuits & Clock Distribution (8:00pm)
Session Chair: Carl Anderson; IBM Research, Yorktown Heights, NY.
Co-Chair: John Stahler; IDT Corp., Santa Clara, CA.
5.a. "CMOS Circuit Design Challenges for High Speed Microprocessors"
Speaker: David Greenhill, Sun Microelectronics; Sunnyvale, CA.
5.b. "L, R and C in High Speed Clock and Signal Lines",
Speaker: Phil Restle; IBM Research, Yorktown Heights, NY.
5.c. "Tradeoffs in High Speed Microprocessor Circuit Design",
Speaker: Sam Naffziger; Hewlett Packard Co., Fort Collins, CO.

Wednesday. June 26.1996
Session Six: The PC: Today and Tomorrow (8:30am)
Session Chair: Jim Slager; Hitachi Micro Systems, Inc.; San Jose, CA.
Session Co-Chair: Richard Crisp; Rambus Inc., Mountain View, CA.
6.a. "The Changing PC Landscape"
Speaker: Michael Slater; MicroDesign Resources, Sebastopol, CA.
6.b. "The Business and Technology Trends that Will Shape PC2OOO"
Speaker: Fred J. Pollack; Intel Corp., Hillsboro, OR.
6.c. "PC Manufacturing"
Speaker: Sheau-Jiung Lee, Acer Labs, San Jose, CA.
6.d. "Meeting PC Multimedia Needs with the Media Processor"
Speaker: Pete Foley; Chromatic Research, Sunnyvale, CA
Workshop Ends Wednesday June 28, 1:00pm